



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

GAU 2186

#23  
pay  
9-17-01

In re Application of

**Gerard Chauvel, et al.**

Serial No.: **08/890,894**

Filed: **07/10/97**

For: **MULTIPLE PROCESSOR APPARATUS HAVING A PROTOCOL  
PROCESSOR INTENDED FOR THE EXECUTION OF A COLLECTION OF  
INSTRUCTIONS IN A REDUCED NUMBER OF OPERATIONS**

**TIF-15767A**

Examiner: **D. Tran**

Art Unit: **2186**

Conf. No.: **5253**

**AMENDMENT - 37 C.F.R. § 1.111**

**RECEIVED**

**SEP 14 2001**

**Technology Center 2100**

Assistant Commissioner for Patents

Washington, D.C. 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that the above correspondence is being deposited with  
the U.S. Postal Service as First-Class Mail in an envelope addressed to:  
Assistant Commissioner for Patents, Washington, DC 20231.

*Elizabeth Austin*  
Elizabeth Austin

*9/5/2001*  
Date

Dear Sir:

Responsive to the Office Action dated May 15, 2001, and the Office Communication  
dated August 14, 2001, please amend the above-identified application as follows:

**IN THE SPECIFICATION:** (marked-up version)

Page 1, lines 1-6, change:

[The present invention relates to processors and more particularly concerns protocol  
processors.

The tendency to denser and denser integration of computer hardware leads to the  
requirement to have greater and greater computational power available for this hardware.]  
to instead be: